

FIG.1

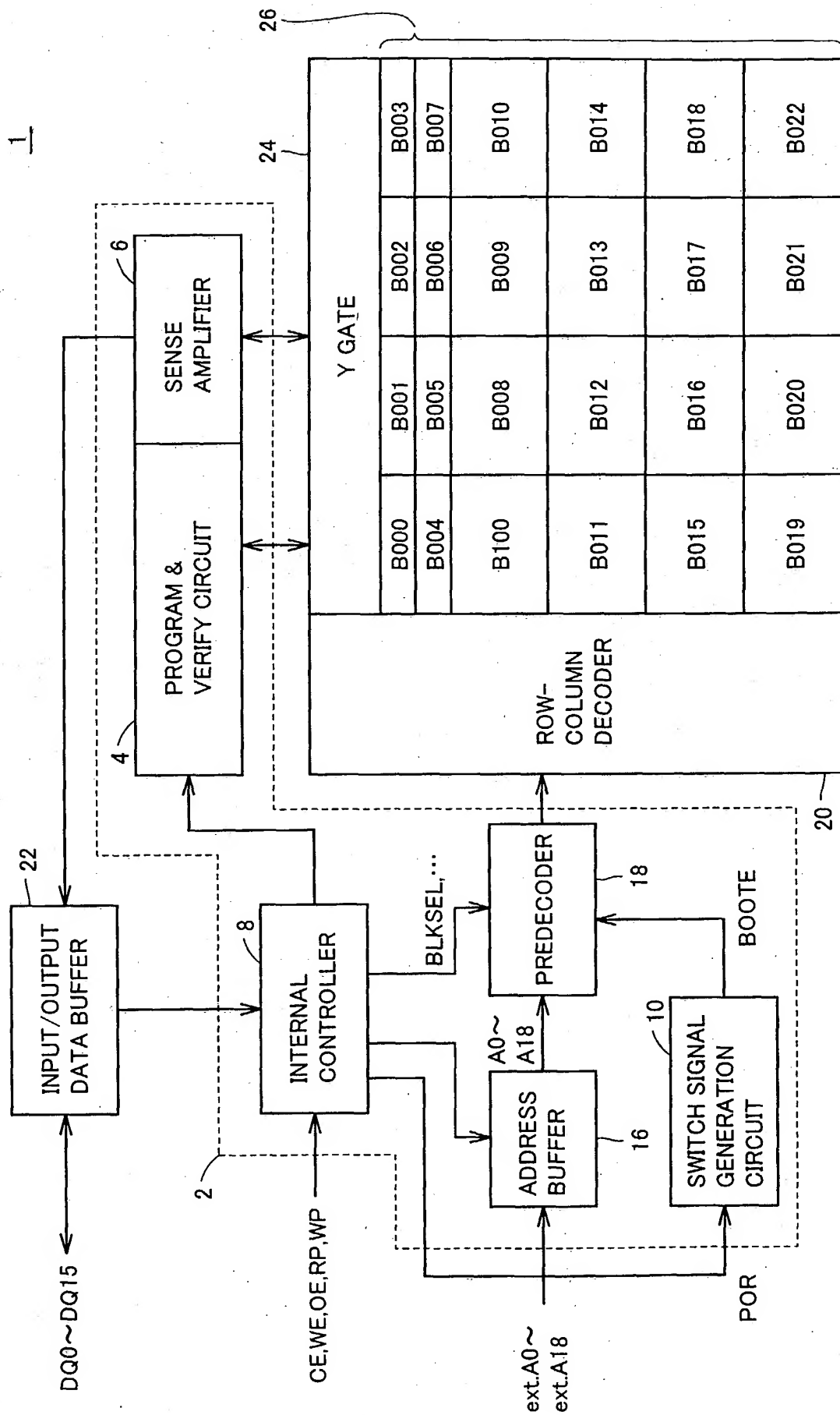


FIG.2

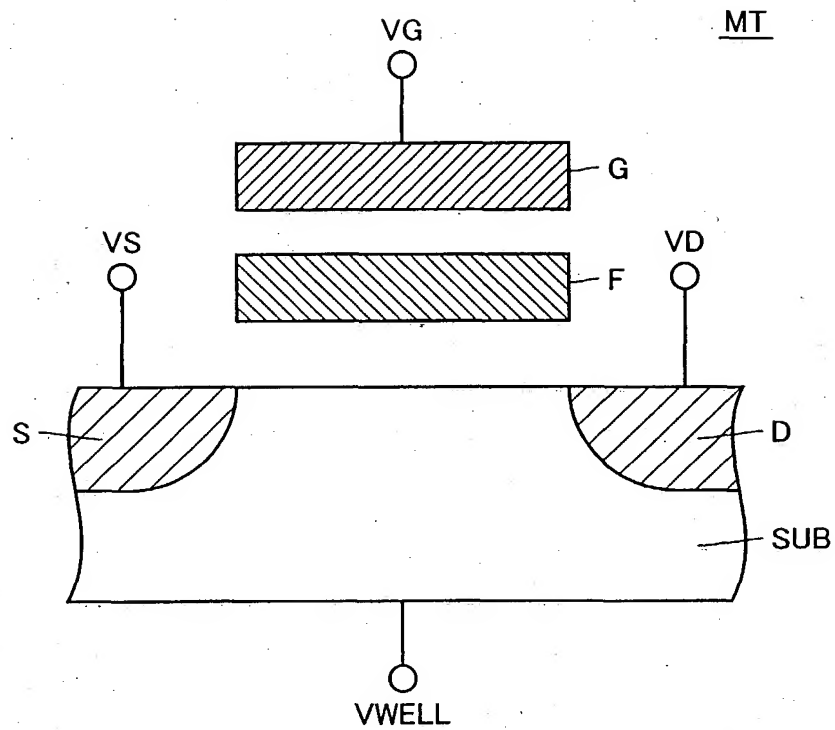


FIG.3

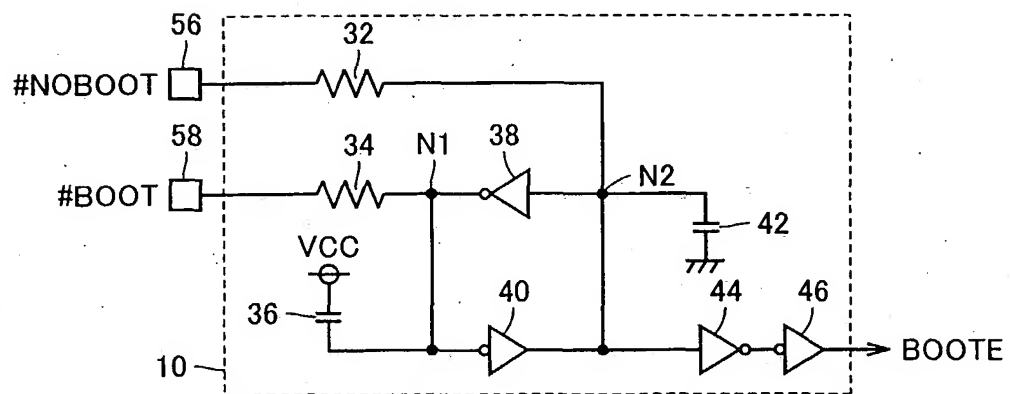


FIG.4

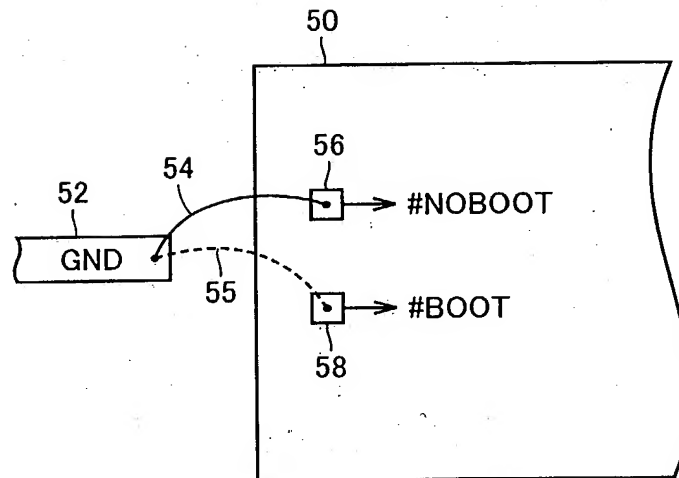


FIG.5

#NOBOOT	#BOOT	BOOTE
L	H or OPEN	L
H or OPEN	L	H

FIG. 6

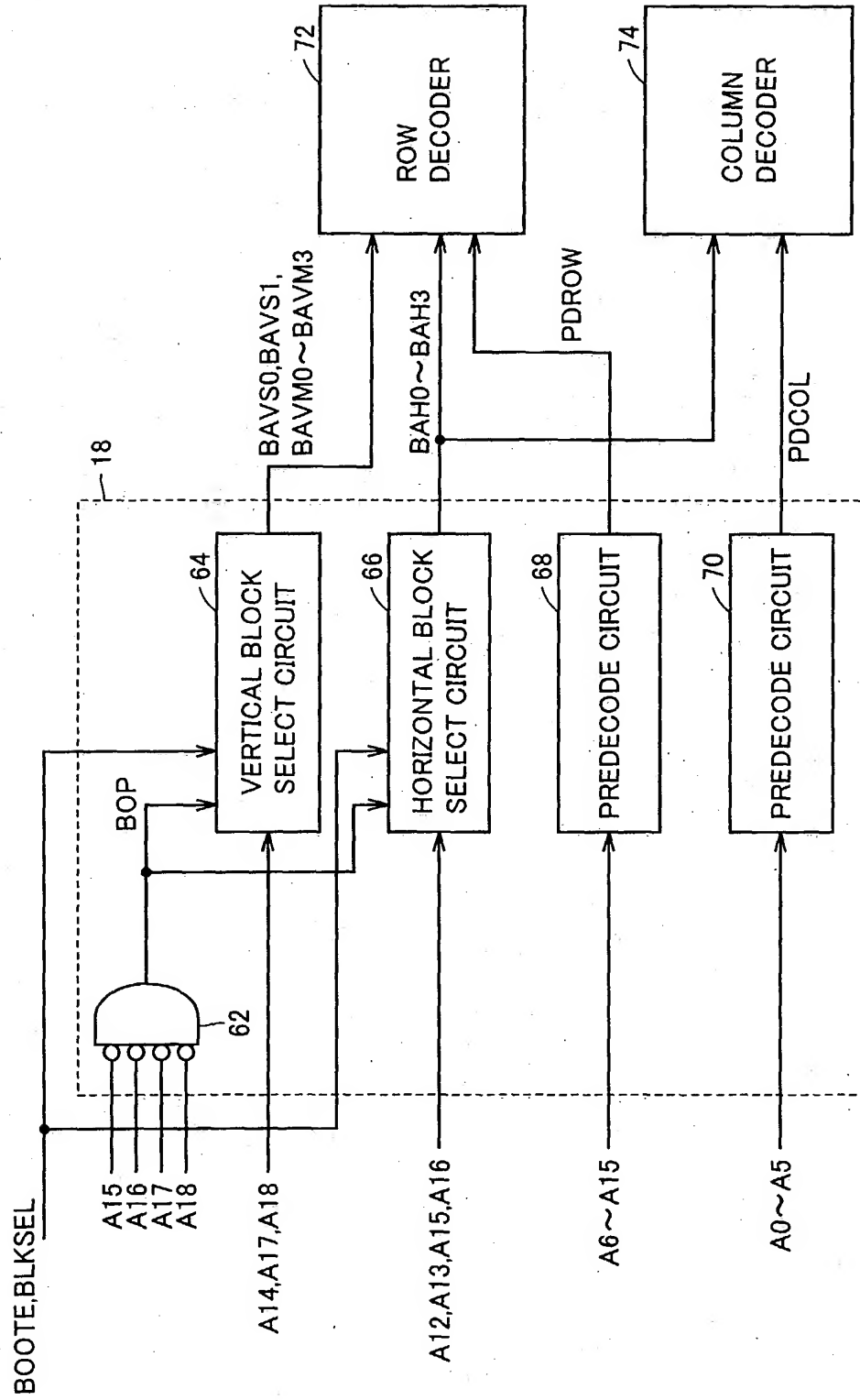


FIG. 7

64

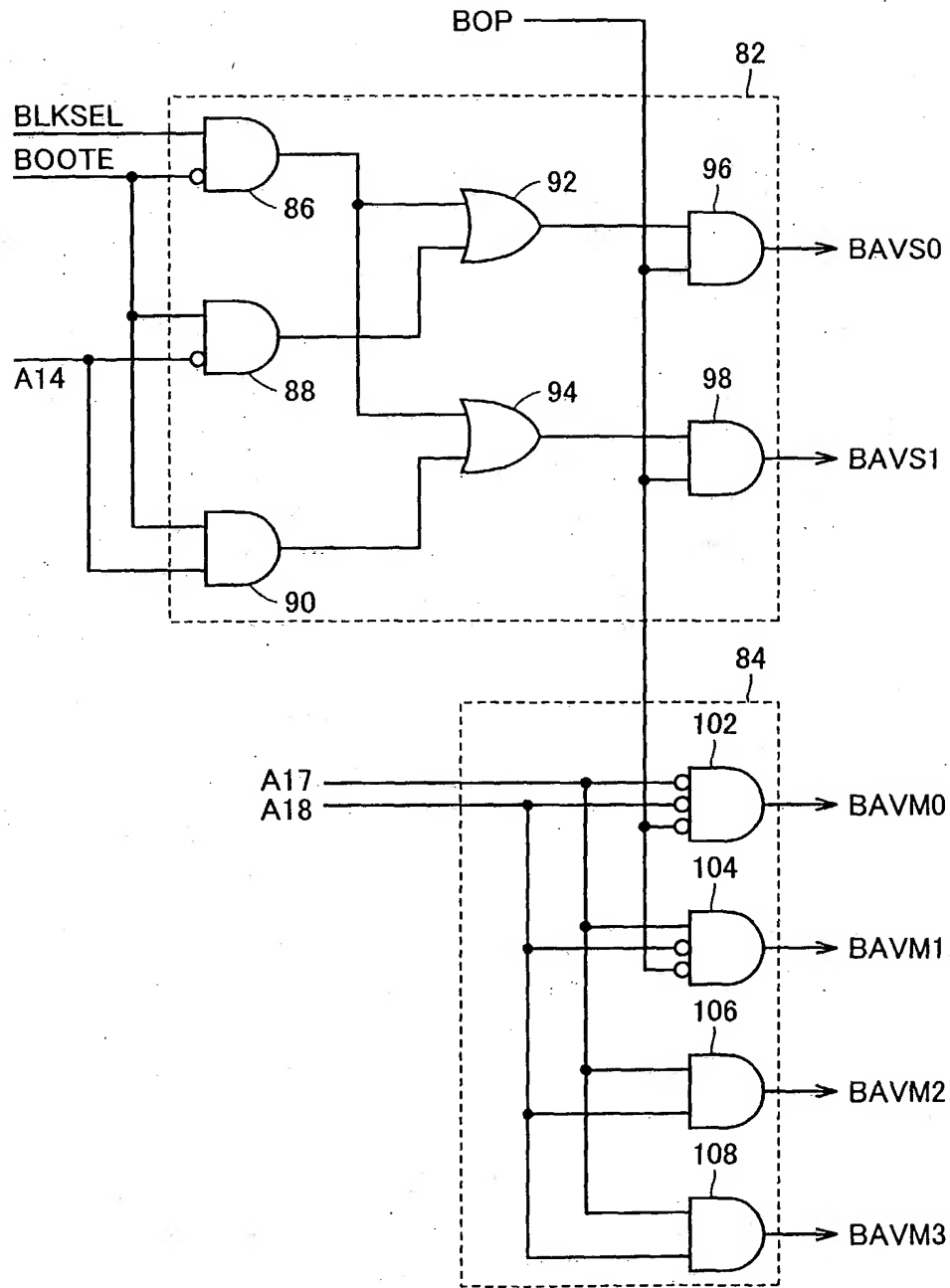
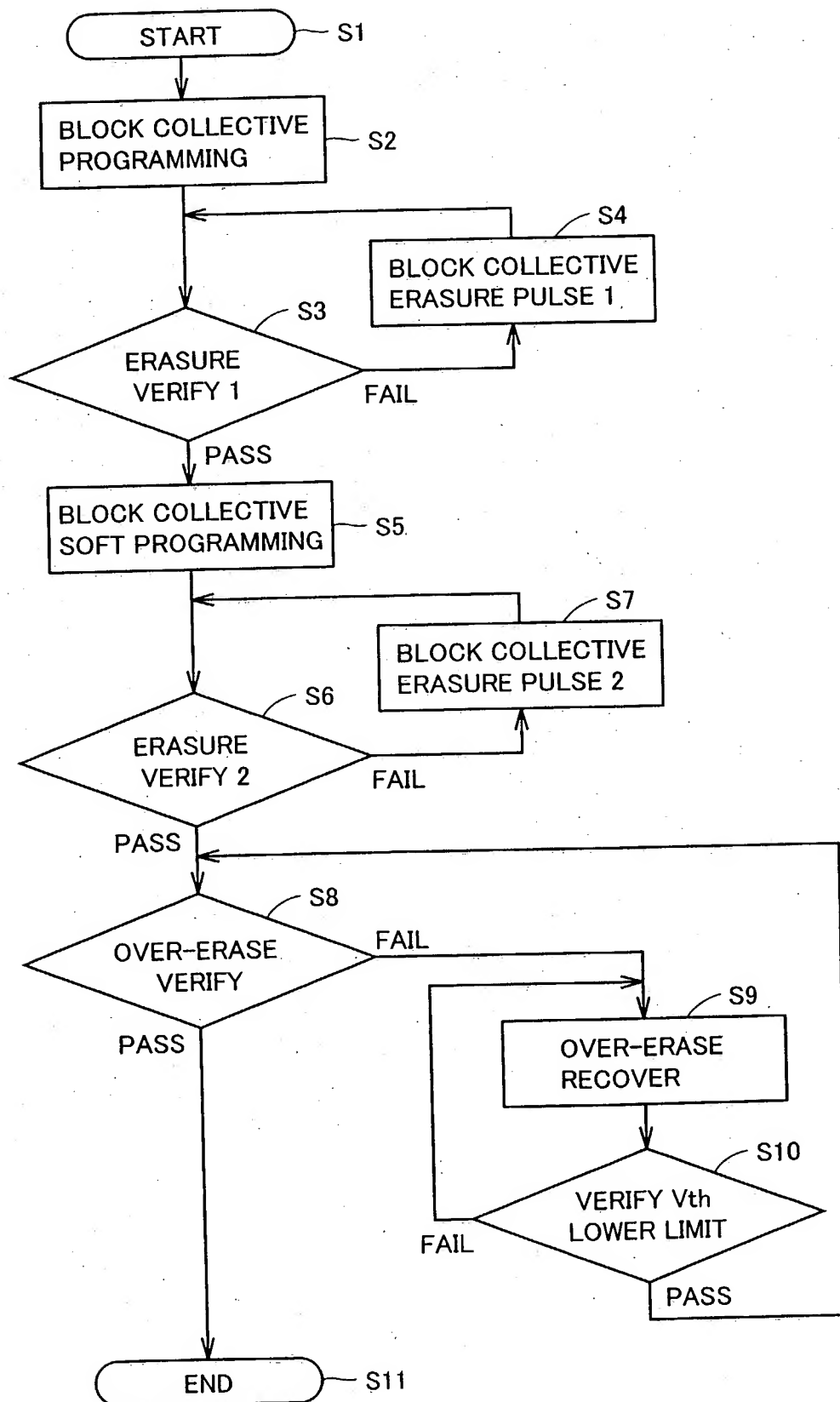


FIG.9



#BOOT	BOOTE
L	H
H or OPEN	L

FIG.12

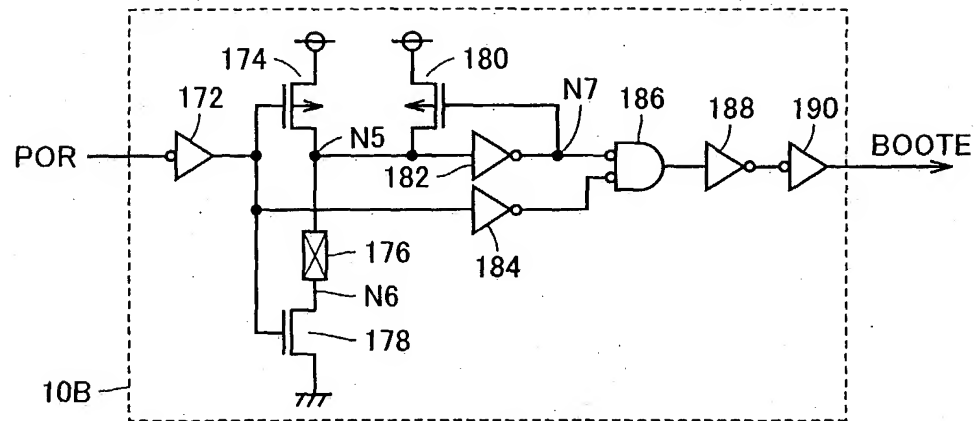


FIG.13

LT FUSE	BOOTE
BURN OUT	H
CONDUCT	L

FIG.14

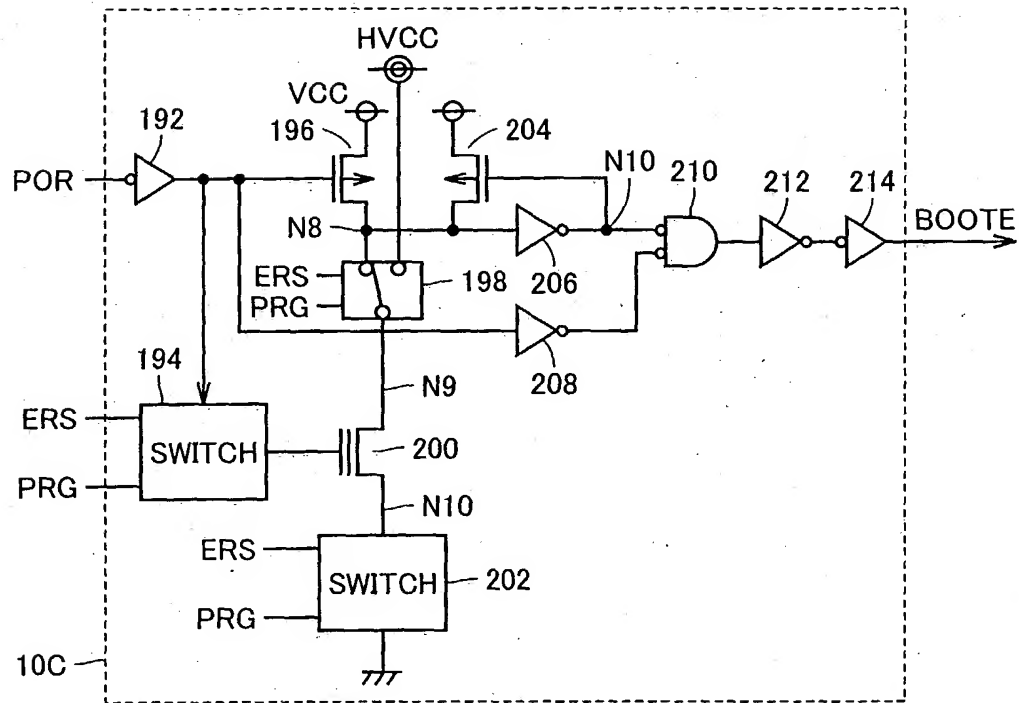


FIG.15

MEMORY TRANSISTOR V _{th}	BOOTE
HIGH	H
LOW	L

FIG. 16

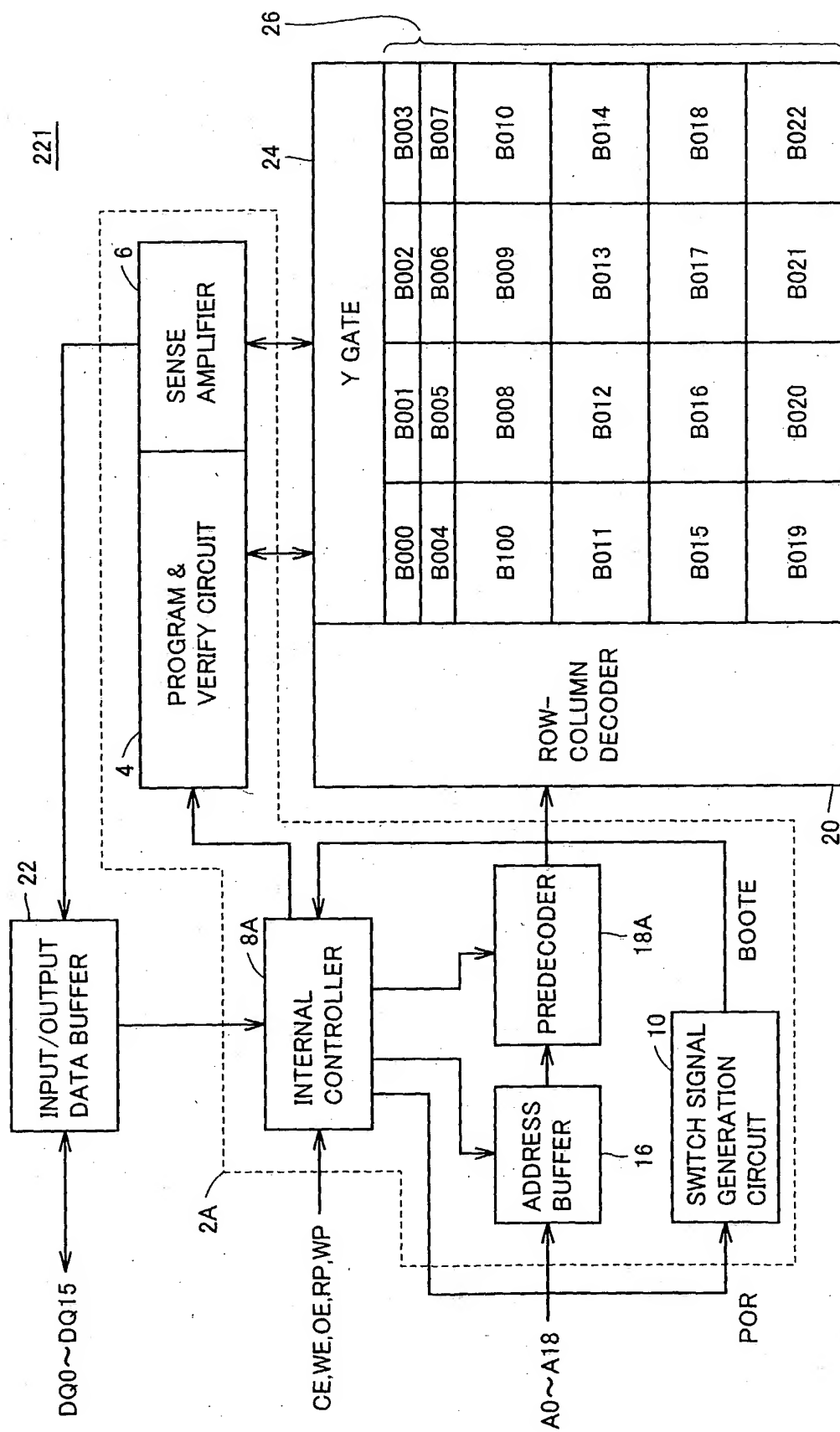


FIG.17

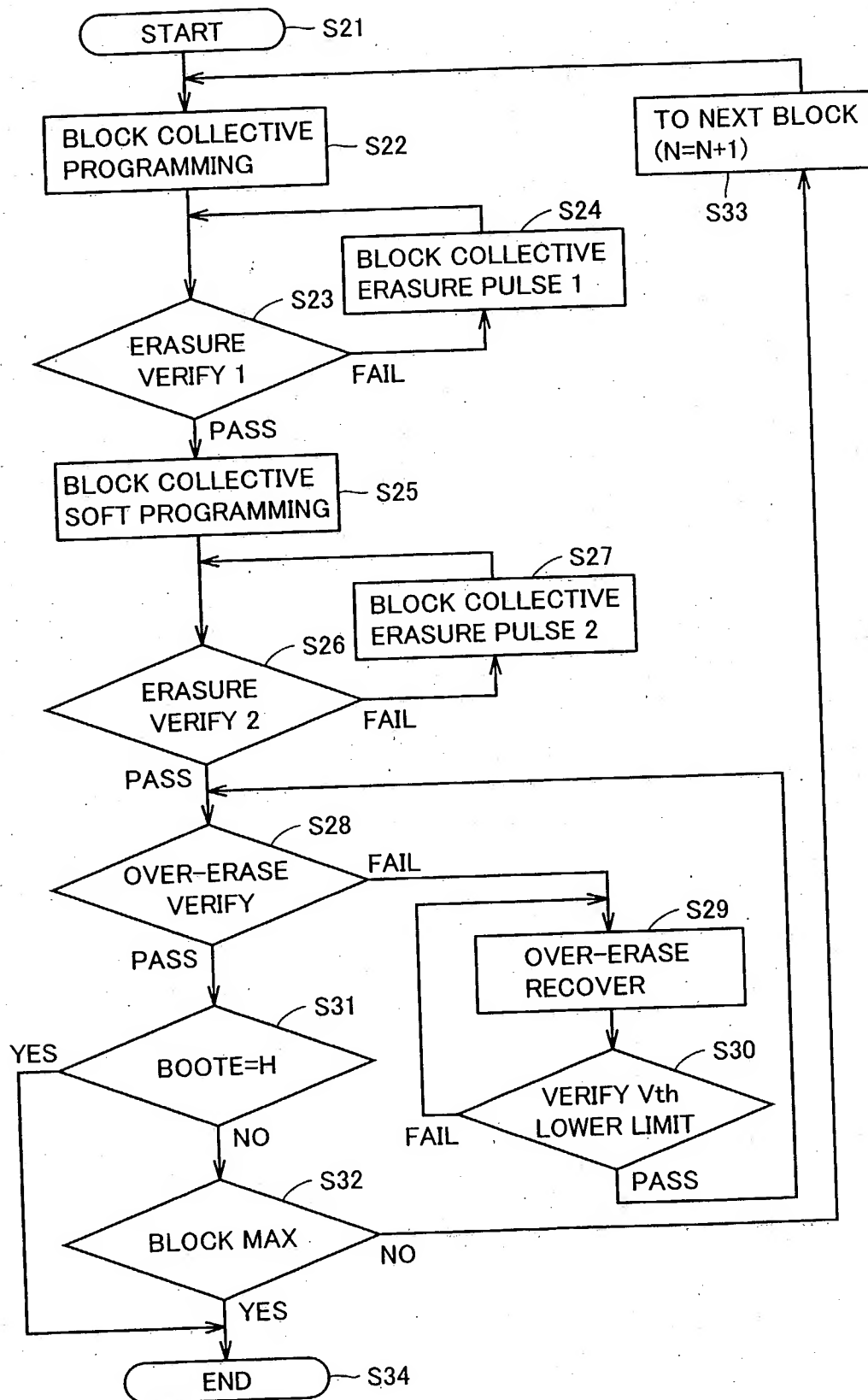


FIG.18

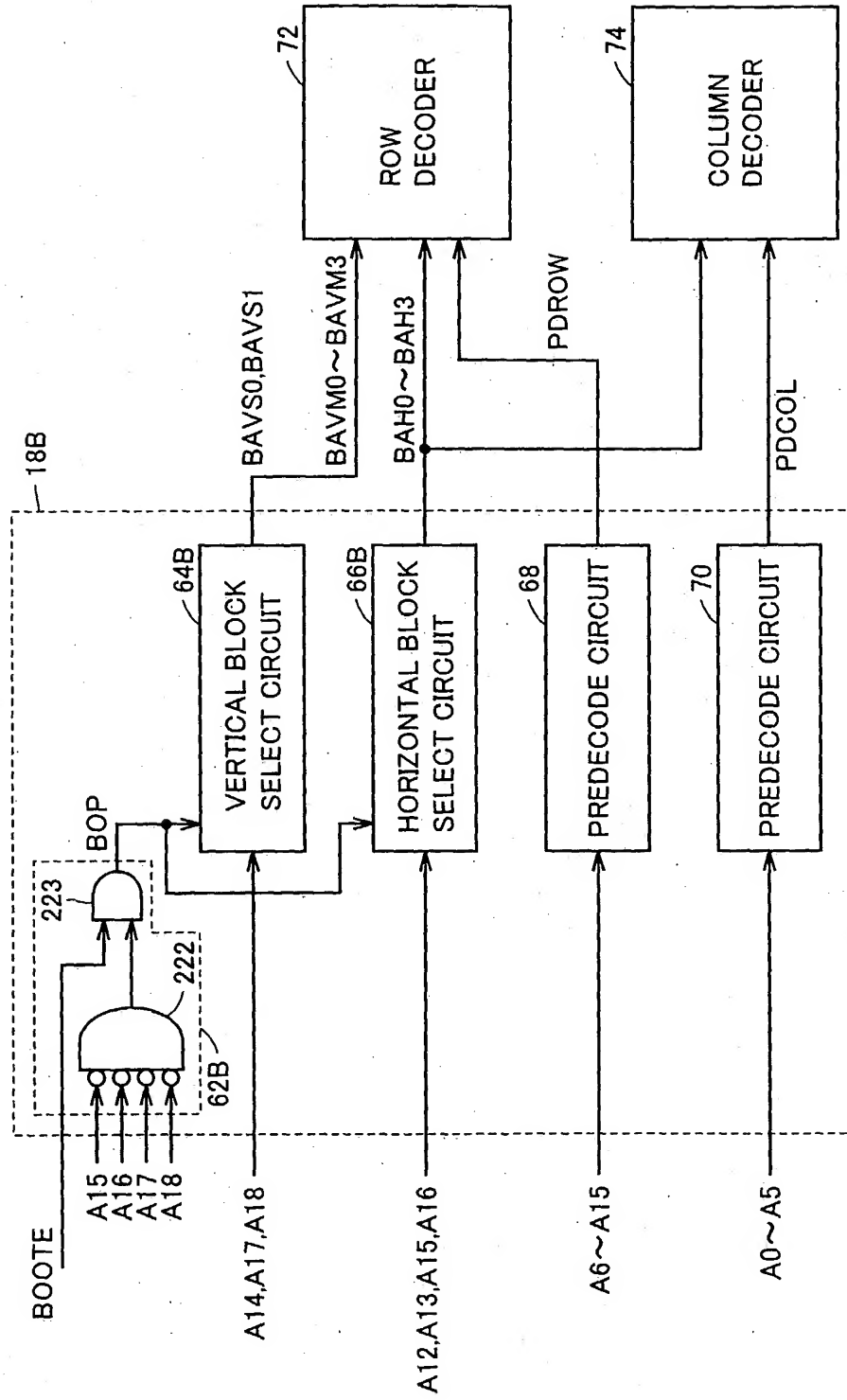


FIG.19

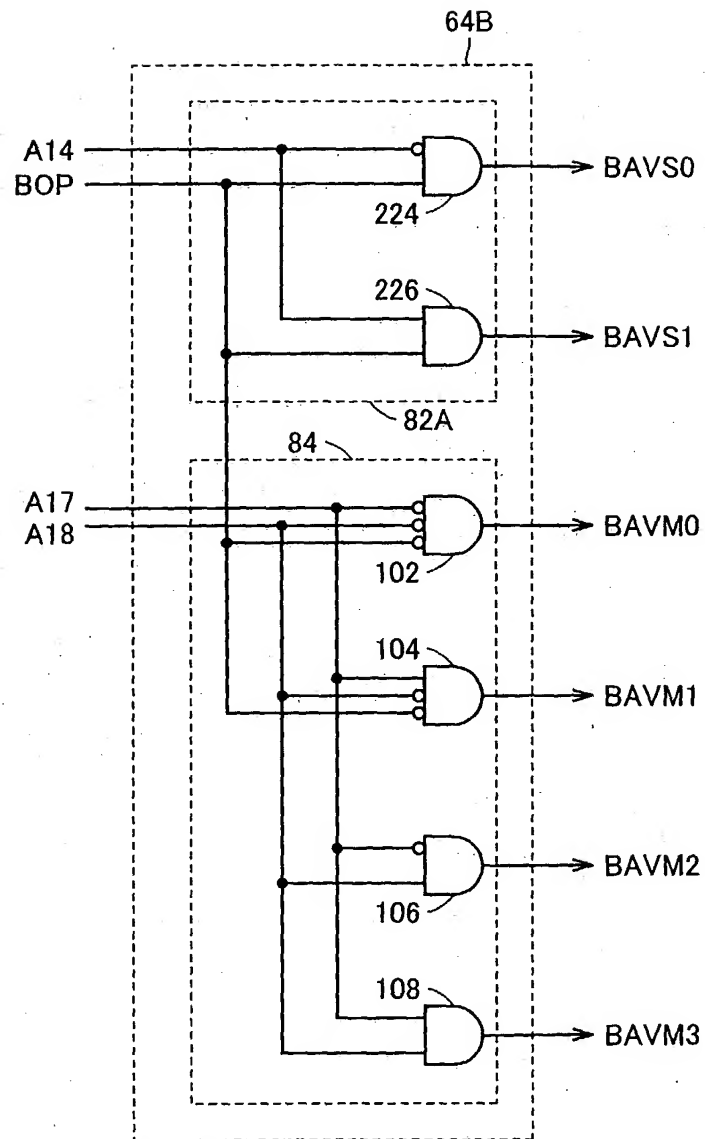


FIG.20

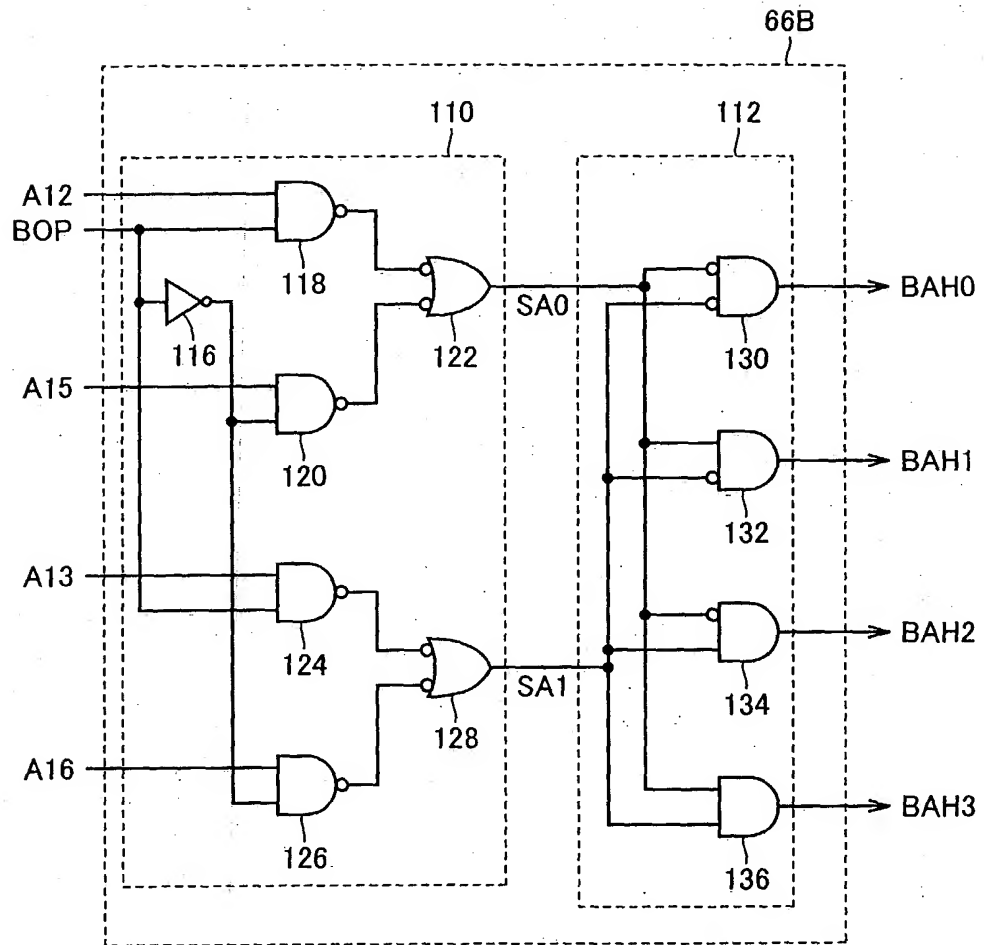


FIG.21

300

CORRESPONDING TO BOTTOM BOOT (B000	B001	B002	B003
	B004	B005	B006	B007
	B100	B008	B009	B010
	B011	B012	B013	B014
	B015	B016	B017	B018
	B019	B020	B021	B200
CORRESPONDING TO TOP BOOT (B022	B023	B024	B025
	B026	B027	B028	B029

FIG.22

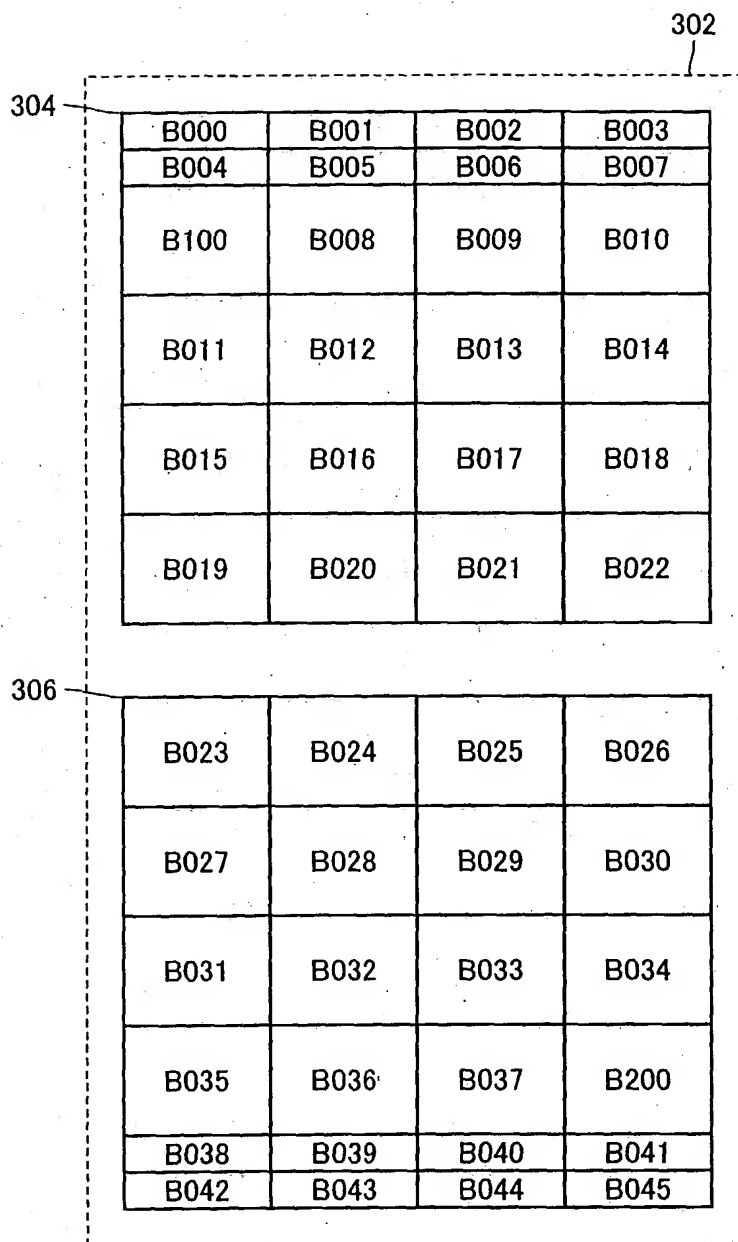


FIG.23

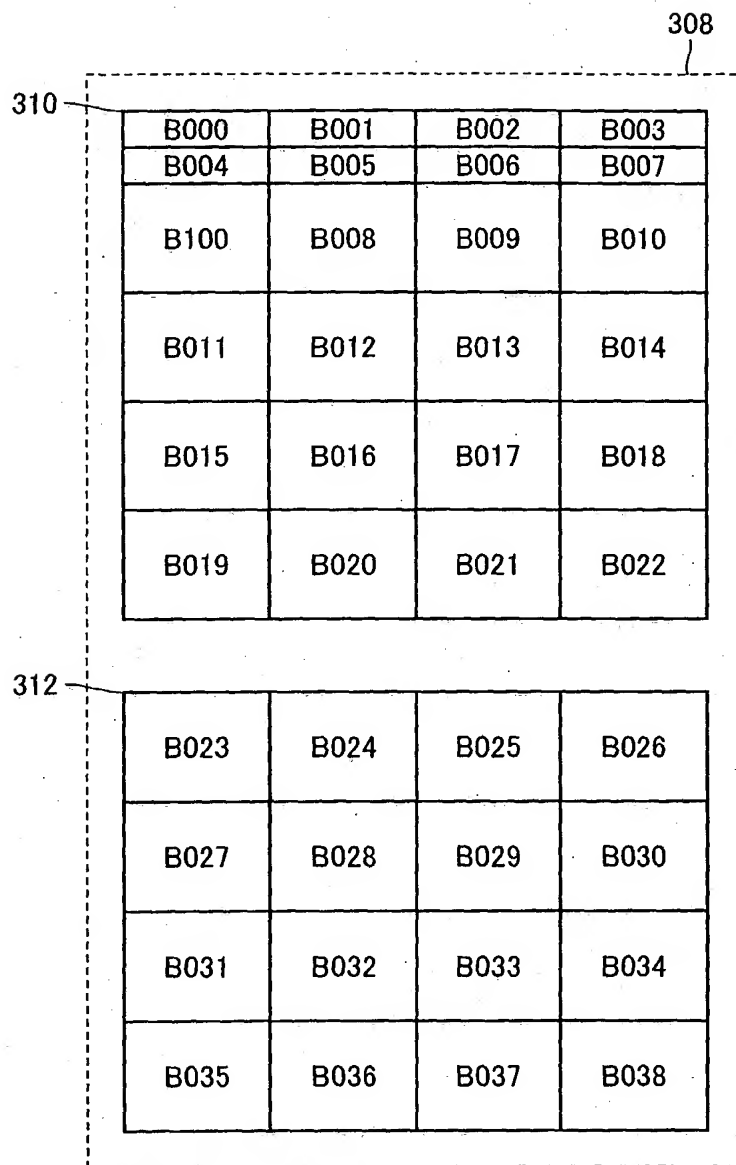


FIG.24

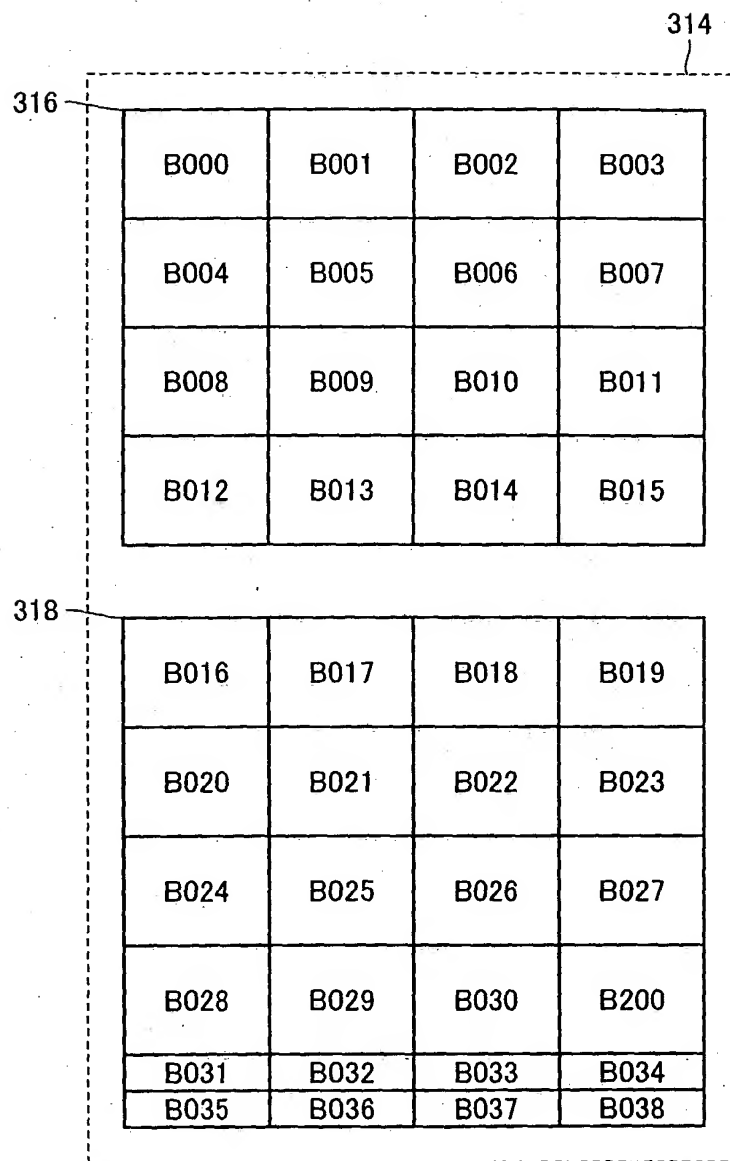


FIG.25

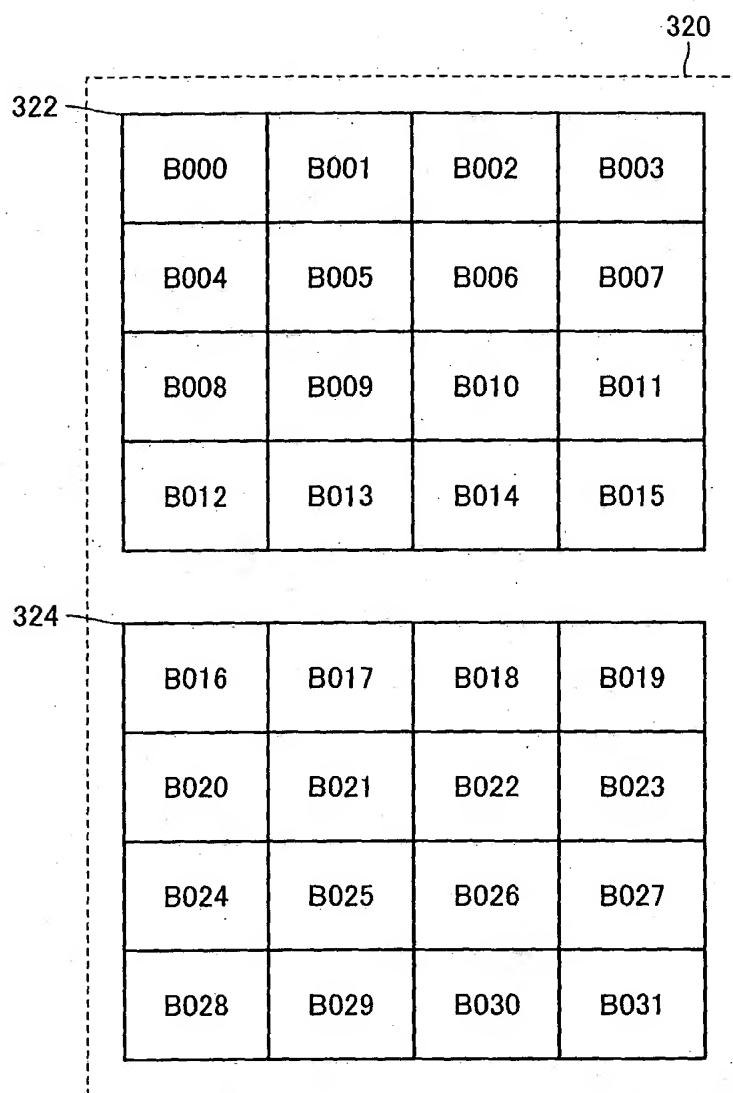


FIG.26 PRIOR ART

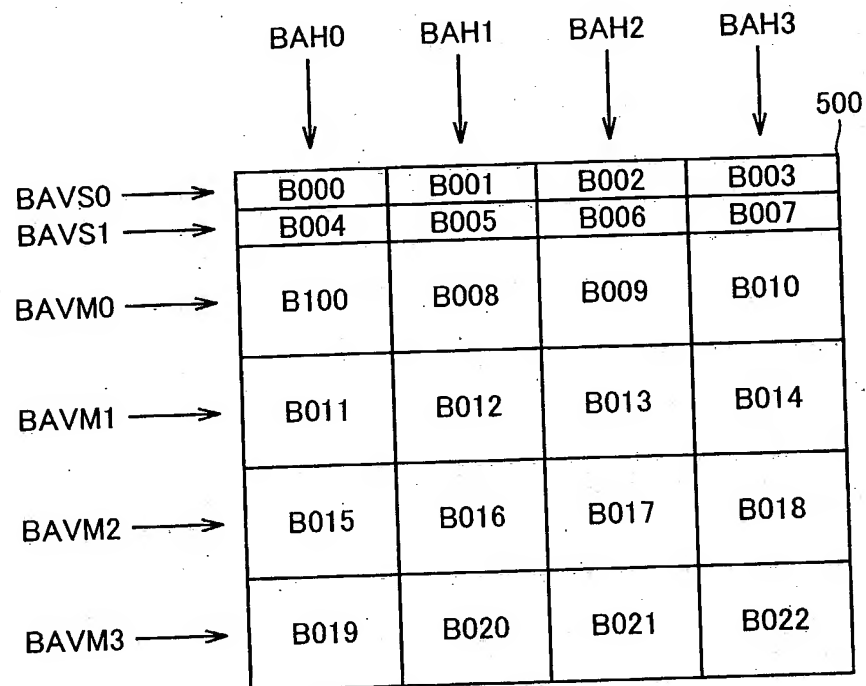


FIG.27 PRIOR ART

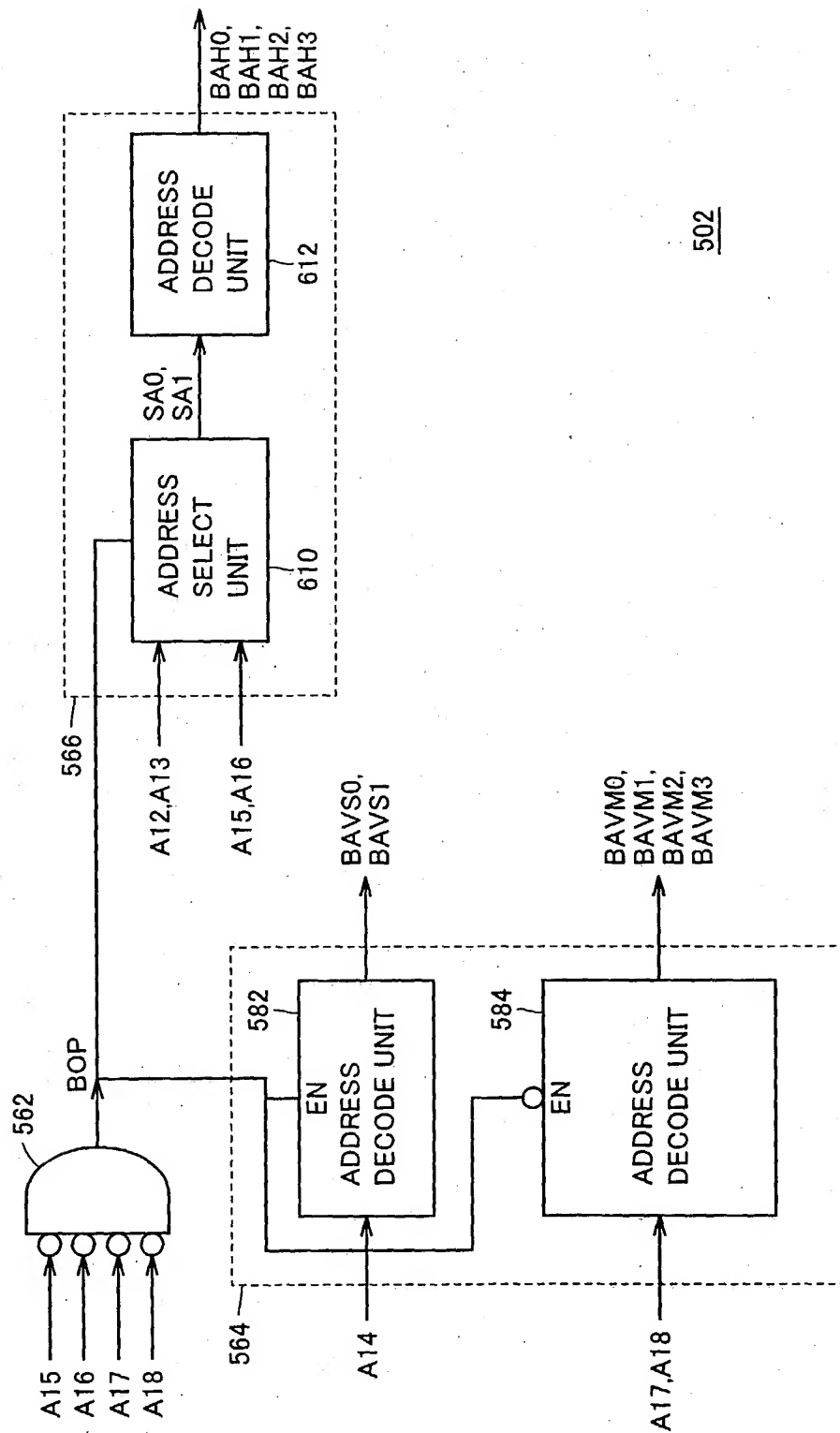


FIG.28 PRIOR ART

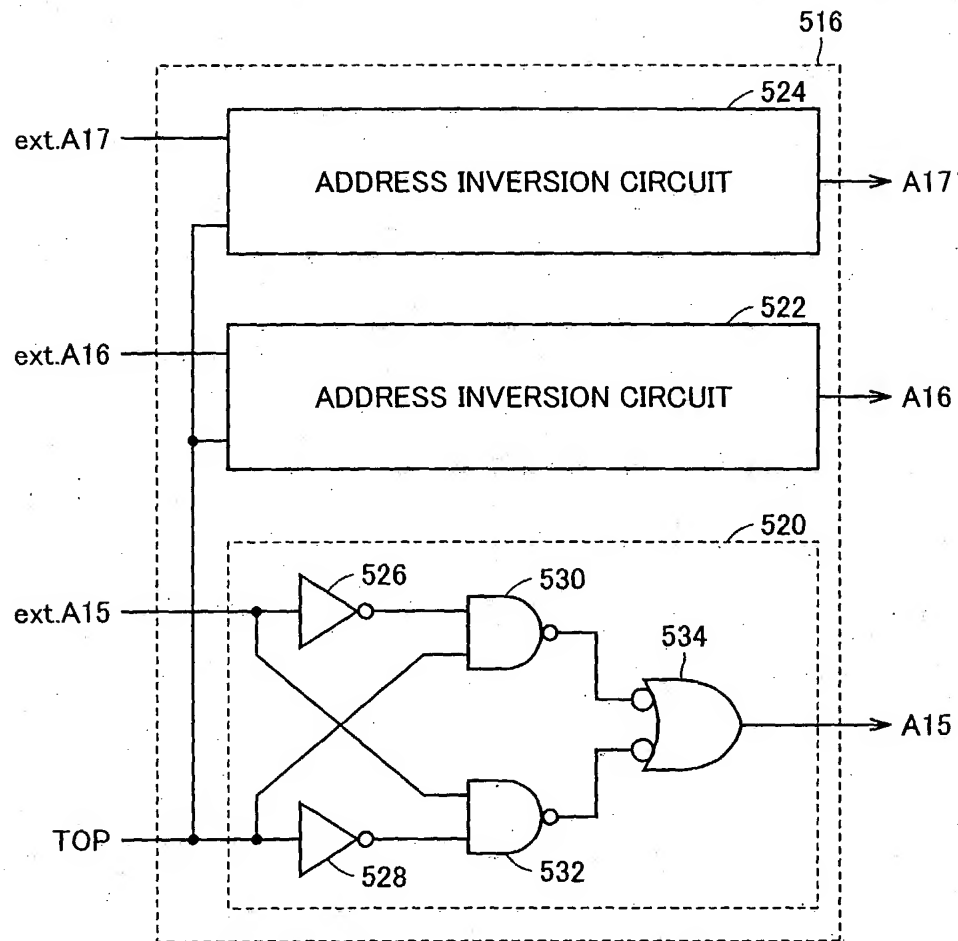


FIG.29 PRIOR ART

700

B000	B001	B002	B003
B004	B005	B006	B007
B008	B009	B010	B011
B012	B013	B014	B015